



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
-----------------	-------------	----------------------	---------------------	------------------

09/769,721

01/25/2001

Kurt E. Spears

10002651-1

5042

7590

09/20/2006

HEWLETT-PACKARD COMPANY

Intellectual Property Administration

P.O. Box 272400

Fort Collins, CO 80527-2400

EXAMINER

AGGARWAL, YOGESH K

ART UNIT

PAPER NUMBER

2622

DATE MAILED: 09/20/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/769,721	Applicant(s) SPEARS ET AL.	
	Examiner Yogesh K. Aggarwal	Art Unit 2622	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 21 August 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1,4-6 and 9 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,4-6 and 9 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

Response to Arguments

1. Applicant's arguments filed 08/21/2006 have been fully considered but they are not persuasive.

Examiner's response:

2. Applicant argues with regards to claim 5 that Yu fails to teach "charges from contiguous blocks from more than one array of photosensors are interleaved in the charge shift register" because the structure identified by the single number "1" is two different charge shift registers. The Examiner respectfully disagrees. The claim lines 3 and 4 recite "transferring charges, from a first contiguous block of photosensors in the first array of photosensors, to **a charge shift register...**". The claim does not recite "**only** a charge shift register". Therefore the charges from red and blue (contiguous blocks) are interleaved on the pair of charge shift register number 1. Hence, in response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., only a single charge register) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

3. Applicant argues with regards to claim 9 that Toma fails to teach "transferring a block of photosensors in an array of photosensors directly to a charge shift register without any intervening charge shift registers" because 105 and 505 are elements of a charge shift registers and not photosensors, and the vertical charge shift registers VR1-VR8 are intervening charge shift registers between the photosensors and the charge shift registers 3. The Examiner respectfully disagrees. The claim, line 5 recites "...wherein the block **comprises** less than all the

photosensors...” which is an open-ended statement. The block of photosensors is being read as a combination of photosensors and vertical CCDs. The claim does not recite “ wherein the block consists less than all the photosensors”. Therefore charges are transferred from first block of photosensors (photosensors and V-CCDs as an array) directly to the horizontal CCD without any intervening charge shift registers.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claim 5 is rejected under 35 U.S.C. 102(b) as being anticipated by Yu (US Patent # 5,345,319).

[Claim 5]

Yu teaches a method of scanning comprising exposing, first (figure 2, blue 5) and second arrays (figure 2, red 3) of photosensors to light, transferring charges, from a first contiguous block of photosensors (the blue color array comprises a contiguous first block of photosensors) in the first array of photosensors (figure 2, blue 5), to a charge shift register (figure 2, element 1), wherein the block comprises less than all the photosensors (blue color pixels comprise less than the total number of all the photosensors RGB), and only charges from the first block are transferred; transferring charges from a second contiguous block of photosensors (the red color array comprises a contiguous second block of photosensors) in the second array of photosensors (figure 2, red 3), to the charge shift register (figure 2, element 1, it is noted that the charges are

Art Unit: 2622

transferred to the same charge shift register as the first block), where only the charges from the second block are transferred, so that charges from contiguous block from more than one array of photosensors are simultaneously interleaved onto the charge shift register (col. 3 line 64- col. 4 line charges from the first block of photosensors comprised of blue color pixels in a first array of photosensors 5 transfer charges to the charge shift registers 1 and the block of red pixels 3 simultaneously comprise less than all the photosensors RGB and these charges are interleaved in the SHIFT register 1. Yu discloses that the signal charges generated in PD arrays 30 and 50 are integrated for a predetermined time period and then shifted to the HCCD analog shift register 10 which would be obvious to one skilled in the art that they are being simultaneously to the charge shift register).

Claim Rejections - 35 USC § 102

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

7. Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by Yonemoto (US Patent # 6,441,851).

[Claim 1]

A method of scanning comprising exposing an array of photosensors to light, a first time (e.g. long exposure time from t1 to t4 in figure 2)

Art Unit: 2622

activating a particular section of a charge transfer gate, where the charge transfer gate has a plurality of sections, each section individually controllable, and fewer than all the sections are activated (e.g. figure 2 shows that during long exposure time, gates V2a and V2b is activated at times t1 and t4 respectively out of the gates V1, V2a, V3 and V2b wherein each section is shown to be individually controllable);

transferring charges, transferring charges from a contiguous block of the photosensors through the particular section of the charge transfer gate, to a charge shift register (e.g. charges corresponding to photosensors 1 and 2 are transferred to charge shift register during long exposure time at t2 and t4) ,

exposing, the array of photosensors, to light, a second time (e.g. short exposure time from t5 to t8 in figure 2)

transferring charges, from the contiguous block of photosensors through the particular section of the charge transfer gate, to the charge shift register, so that the charges from the contiguous block of photosensors, from more than one exposure, are interleaved in the charge shift register (e.g. charges corresponding to same block of photosensors 11 and 12 are transferred through the same charge transfer gate to the charge shift register and interleaved with the charges from the long exposure time).

8. Claim 9 is rejected under 35 U.S.C. 102(e) as being anticipated by Toma et al. (US Patent # 6,707,498).

[Claim 9]

Toma et al. teaches a method of scanning comprising exposing, first (horizontal row comprising charges 105 and 505 is read as first array, See figure 10 and 11) and second arrays (horizontal

Art Unit: 2622

row comprising charges 109 and 509 is read as second array, See figure 12) of photosensors to light;

transferring charges, from a first block of photosensors in the first array of photosensors, directly to a charge shift register without any intervening charge shift registers, wherein the block comprises less than all the photosensors, and only charges from the first block are transferred (e.g. charges from a first block of photosensors 105 and 505 are read into H4 and H8 as shown in figure 10 and 11);

shifting, in the charge shift register, the charges from the first block of photosensors (figure 12 shows the charges 105 and 505 being shifted into H3 and H7).

transferring charges, from a second block of photosensors in the second array of photosensors, directly to the charge shift register without any intervening charge shift registers, into the stages of the charge shift register previously occupied by the charges from the first block of photosensors before shifting, where only the charges from the second block are transferred so that charges from blocks from more than one array of photosensors are interleaved onto the charge shift register (figure 12 shows charges 109 and 509 being shifted into H4 and H5 and interleaved with charges from the first block of photosensors).

Claim Rejections - 35 USC § 103

9. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Art Unit: 2622

10 . Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yonemoto (US Patent # 6,441,851) as applied to claim 1 above and in further view of Hynecek (US Patent # 6,459,077).

[Claim 4]

Yonemoto teaches the limitations of claim 1 but fails to teach "... shifting charges, within the charge shift register, at a lower than normal shift rate". However Hynecek teaches that these limitations are well known and used in the art (col. 3 lines 35-43). Therefore taking the combined teachings of Yonemoto and Hynecek it would have been obvious to one skilled in the art at the time of the invention to have been motivated to shift charges at a lower than normal shift rate. The benefit of doing so would be to improve the bucket brigade charge transfer efficiency to be similar to that expected of typical CCD devices as taught in Hynecek (col. 3 lines 35-37).

11 . Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yu (US Patent # 5,345,319) as applied to claim 5 above and in further view of Hynecek (US Patent # 6,459,077).

[Claim 6]

Yu teach the limitations of claim 5 but fails to teach "... shifting charges, within the charge shift register, at a lower than normal shift rate". However Hynecek teaches that these limitations are well known and used in the art (col. 3 lines 35-43). Therefore taking the combined teachings of Yu and Hynecek it would have been obvious to one skilled in the art at the time of the invention to have been motivated to shift charges at a lower than normal shift rate. The benefit of doing so would be to improve the bucket brigade charge transfer efficiency to be similar to that expected of typical CCD devices as taught in Hynecek (col. 3 lines 35-37).

Conclusion

12. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yogesh K. Aggarwal whose telephone number is (571) 272-7360. The examiner can normally be reached on M-F 9:00AM-5:30PM.

13. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Vivek Srivastava can be reached on (571)-272-7304. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2622

14. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

YKA

September 13, 2006



VIVEK SRIVASTAVA
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600